



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

DOTTA et al

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Serial No. 10/670,244

Group: 2811

Filed: September 26, 2003

Examiner:

For: SEMICONDUCTOR DEVICE AND CHIP-STACK
SEMICONDUCTOR DEVICE

* * * * *

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

REQUEST FOR CORRECTION OF FILING RECEIPT

Enclosed is a copy of the Filing Receipt for the above-identified application which incorrectly states the second inventor's name as "Toshi Kimura". It should read "Toshio Kimura". The correction is shown in red on the attached copy of the filing receipt. Please note your records accordingly. Correction of same is respectfully requested.

Respectfully submitted,

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January 29, 2004

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1035-472



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CONFIRMATION NO. 8593

FILING RECEIPT



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Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Assignment For Published Patent Application

SHARP KABUSHIKI KAISHA, Osaka, JAPAN;

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Non-Publication Request: No

Early Publication Request: No

Title

Semiconductor device and chip-stack semiconductor device